



UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Sanchez et al. GROUP ART UNIT: 2123
APPLN. NO.: 09/877,792 EXAMINER: Proctor, Jason Scott
FILED: 8 June 2001
TITLE: METHOD AND APPARATUS FOR DESIGNING AND MAKING AN
INTEGRATED CIRCUIT

Certificate of MailingDate of deposit: 2-16-05

I hereby certify that this paper is being deposited with the United States Postal Service on the date indicated above, as first-class mail, with sufficient postage attached thereto, in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313.

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AMENDMENT AND RESPONSE TO NON-FINAL OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated 21 October 2004 and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer. Applicants respectfully request reconsideration of the pending claims in light of Applicants' amendments and remarks.

A Petition for Extension of Time to respond, with fee authorization, is submitted concurrently herewith. If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.